

Sub A Claim 1. ~~A method for controlling the operating condition of a computer processor comprising the steps of:~~

- 3 determining a maximum allowable power consumption level from the operating condition of the processor,
- 5 determining a maximum frequency which provides power not greater than the allowable power consumption level,
- 7 determining a minimum voltage which allows operation at the maximum frequency determined, and
- 9 dynamically changing the operating condition of the processor by changing the frequency and voltage to the maximum frequency and ~~minimum voltage determined.~~

Sub B Claim 2. A computing device comprising:

- 2 a power supply furnishing selectable output voltages,
- 3 a clock frequency source,
- 4 a central processor including:
 - 5 a processing unit for providing values indicative of operating conditions of the central processor, and
 - 7 a clock frequency generator receiving a clock frequency from the clock frequency source and providing a selectable output clock frequency to the processing unit; and

10 means for detecting the values indicative of operating conditions of the
11 central processor and causing the power supply and clock frequency
12 generator to furnish an output clock frequency and voltage level for the
13 central processor.

1 Claim 3. A computing device as claimed in Claim 2 in which the
2 means for detecting the values indicative of operating conditions of the
3 central processor comprises control software for determining an output
4 clock frequency and voltage level for the central processor adapted to
5 conserve power while maintaining an effective execution rate.

1 Claim 4. A computing device as claimed in Claim 2 in which the clock
2 frequency generator provides a plurality of selectable output clock
3 frequencies, and

4 the means for detecting the values indicative of operating conditions of
5 the central processor causes the clock frequency generator to generate
6 frequencies which are selected for optimum operation of a plurality of
7 functional units of the computing device.

1 Claim 5. A method for controlling the power used by a computer
2 comprising the steps of:
3 utilizing control software to measure the operating characteristics of a
4 central processor of the computer,

5 determining when the operating characteristics of the central processor
6 are significantly different than required by the operations being
7 conducted, and

8 changing the operating characteristics of the central processor to a level
9 commensurate with the operations being conducted.

Claim 6. A method as claimed in Claim 5 in which:

2 the step of determining when the operating characteristics of the central
3 processor are significantly different than required by the operations being
4 conducted comprising utilizing the control software to determine
5 desirable voltages and frequencies for the operation of the central
6 processor based on the measured operating characteristics, and

7 the step of changing the operating characteristics of the central
8 processor to a level commensurate with the operations being conducted
9 comprises providing signals:

10 for controlling voltages furnished by a programmable power supply
11 to the central processor, and

12 for controlling frequencies furnished by the central processor to
13 the central processor.

1 Claim 7. A method as claimed in Claim 6 in which the step of
2 changing the operating characteristics of the central processor to a level
3 commensurate with the operations being conducted comprises also
4 providing signals for controlling frequencies furnished by the central
5 processor to other functional units of the computer.

1 Claim 8. A computer comprising:

2 a power supply furnishing selectable output voltages,

3 a clock frequency source,
4 a bus,
5 system memory,
6 a central processor including:
7 a processing unit for providing values indicative of operating
8 conditions of the central processor, and
9 a clock frequency generator receiving a clock frequency from the
10 clock frequency source and providing a selectable output clock
11 frequency to the processing unit; and
12 means for detecting the values indicative of operating conditions of the
13 central processor and causing the power supply and clock frequency
14 generator to furnish an output clock frequency and voltage level for the
15 central processor.

1 Claim 9. A computer as claimed in Claim 8 in which the means for
2 detecting the values indicative of operating conditions of the central
3 processor comprises control software for determining an output clock
4 frequency and voltage level for the central processor adapted to conserve
5 power while maintaining an effective execution rate.

1 Claim 10. A computing device as claimed in Claim 8 in which the clock
2 frequency generator provides a plurality of selectable output clock
3 frequencies, and

4 the means for detecting the values indicative of operating conditions of
5 the central processor causes the clock frequency generator to generate
6 frequencies which are selected for optimum operation of a plurality of
7 functional units of the computing device including system memory.

1 Claim 11. A computing device as claimed in Claim 8 in which the clock
2 frequency generator provides a plurality of selectable output clock
3 frequencies, and

4 the means for detecting the values indicative of operating conditions of
5 the central processor causes the clock frequency generator to generate
6 frequencies which are selected for optimum operation of a plurality of
7 functional units of the computing device including the bus.

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